

Bluetooth LE Module ME54BS61



Datasheet V 1.0.0

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Version Note

Version	Details	Contributor(s)	Date	Notes
1.0.0	First edit	Michelle	2025.06.23	

Part Number

Model	Hardware Code		
ME54BS61	4Y15TE		

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ME54BS61-nRF54L15

Ultra-Small Size, Ultra-Low Power Consumption, BLE 6.0 Supported Bluetooth Module

The ME54BS61 is a highly flexible, ultra-low power consumption, cost-effective, and compact-sized Bluetooth module based on the nRF54L15 WLCSP. It features a powerful Arm® Cortex®-M33 CPU with a core operating speed of up to 128MHz, along with 1.5MB of NVM space and 256 KB of RAM. The module comes with an onboard antenna, highlighting the superior performance of the nRF54 series, and provides more GPIOs for development. Its ultra-low system power consumption, excellent RF performance, and powerful supporting resources make it an ideal solution for Bluetooth connectivity.

FEATURES



Bluetooth 6.0



Ultra-small size



Ultra-low Power



Multi-Protocol supports: Bluetooth LE 6.0 Channel Sounding, Bluetooth Mesh, Thread, Matter, and proprietary 2.4 GHz protocols

KEY PARAMETER

ME54BS61			
Chip Model	nRF54L15	Antenna	Antenna-free
Module Size	6.0×6.0×1.8mm	GPIO	30
Flash	1.5MB	RAM	256 KB
Receiving Sensitivity	-96dBm	Transmission Power	-8 ~ +8dBm
Current(TX)	0dBm-5mA	Current(RX)	3.2mA

APPLICATION



Smart Home



Computer Accessories



Augmented reality

Virtual reality and Game controllers



and Remotes





Medical Devices

Industrial IoT



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1 BLOCK DIAGRAM



2 ELECTRICAL SPECIFICATION

Parameter	Values	Notes
Working Voltage	1.7V-3.6V	To ensure proper RF performance, it is recommended that the supply voltage is not lower than 2.3V
Working Temperature	-40°C~+105°C	Operating temperature from -40°C to 105°C
Transmission Power	-8 ~ +8dBm	Configurable
Current(RX)	3.2mA	RF receiving current under 1Mbps pattern
Current(TX)	5mA	RF transmission current under 0dBm pattern
Module Dimension	6.0*6.0*1.8mm	
Quantity of IO Port	30	

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3 PIN DESCRIPTION



Pin Numbe	r Symbol	Туре	Definition
Al	RF	External Antenna	Connect external antenna via this pin
A2/B1/D2/E7/G3	GND	GND	Ground
D7	VCC	Power Supply	
B3	SWDIO	Programming Data	Used for firmware programming
B4	SWCLK	Programming Clock	Used for firmware programming
C2	RST	Reset	Low level reset, high level operation
A3-A7	P2.04/P2.05/P2.03/P2.01/P2.00	GPIO	General-purpose IO
B2/B5-B7	P2.06/P2.02/P1.07/P1.08	GPIO	General-purpose IO
C1/C6 C7	P0.00/P1.03/P1.06	GPIO	General-purpose IO
D1/D6	P0.01/P1.02	GPIO	General-purpose IO
E1/E2 E6	P0.02/P2.10/P1.04	GPIO	General-purpose IO
F1-F7	P0.03/P2.09/P2.08/P1.09/P1.11/P1.14/P1.0	5 GPIO	General-purpose IO
G1-G7	P0.04/P2.07/P1.10/P1.12/P1.13/P1.15	GPIO	General-purpose IO

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Default unit: mm Default tolerance: ±0.15





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7 PCB LAYOUT

There should be no GND plane or metal cross wiring in the module antenna area, and no components should be placed nearby. It is best to make a hollow or clear area, or place it on the edge of the PCB board. The reference example is as follows:

Notice: It is strongly recommended to use the first design method. The module antenna design is debugged according to the first wiring.

Layout Notes:

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1)The module's antenna area should be completely clear of any metal obstructions to avoid affecting antenna performance (as shown in the diagram).

2)Outside the module's antenna area, try to maintain a solid copper pour to minimize interference from the mainboard signal lines or other sources.3)A clear area of at least 4mm should surround the module's antenna (including its casing) to reduce interference with the antenna.

4)Ensure good grounding for components to minimize parasitic inductance.

5)Do not place copper under the module's antenna to prevent interference with signal radiation, which could affect transmission distance.

6)The antenna should be kept away from other circuits to maintain radiation efficiency and avoid impacting the normal operation of other circuits.

7)Position the module as close to the edge of the circuit board as possible, away from other circuitry.

8) It is recommended to use a ferrite bead for isolation when connecting the module to the power supply.

8 REFLOW AND SOLDERING

1) Perform SMT according to the reflow oven temperature profile provided below, with a maximum temperature of 260°C; 2) Follow IPC/JEDEC standards; Peak temperature: < 260°C; Number of reflows: ≤ 2 times; For SMT involving double-sided placement, it is recommended that the module side undergoes reflow soldering only once. For any special processes, please contact our company.

3) Module SMT recommended stencil thickness0.1-0.12mm, pin 1:0.9 opening, not expanded;

4) After opening, if the entire package is not used at once, it should be stored in a vacuum to prevent long-term exposure to air, which can cause moisture absorption and pad oxidation. If there is a gap of 7 to 30 days before reuse, it is recommended to bake the tape at 65-70°C for 24 hours without unrolling it before returning to SMT.

5) ESD protection measures should be implemented before using SMT.



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Packing detai	I Specification	Net weight	Gross weight	Dimension
ME54BS61	1900PCS	TBD	TBD	W=24mm, T=0.30mm
Note: Default weight tolerance all are within 10g (except the special notes)				e special notes)

10 STORAGE CONDITIONS

- Please use this product within 6 months after signing the receipt.
 - This product should be stored without opening the package at an ambient temperature of $5\sim35^{\circ}$ C and a humidity of $20\sim70\%$ RH.
 - This product should be left for more than 6 months after receipt and should be confirmed before use.
 - The product must be stored in a non-corrosive gas (CI2, NH3, SO2, NOx, etc.).
 - To avoid damaging the packaging material, do not apply any excessive mechanical shocks, including but not limited to sharp objects adhering to the packaging material and product dropping.

This product is suitable for MSL2 (based on JEDEC standard J-STD-020).

- After opening the package, the product must be stored at \leq 30°C/<60%RH. It is recommended to use the product within 3-6 months after opening the package.
- When the color of the indicator in the package changes, the product should be baked before welding.
- Baking is not required for one year if exposure is limited to <30°C and 60%RH. Refer to MSL2 for exposure criteria for moisture sensitivity level. If exposed to (≥168h@85°C/60%RH) conditions or stored for more than one year, recommended baking conditions.

1. 120 +5/-5°C, 8 hours, 1 time

Products must be baked individually on heat-resistant trays because the materials (base tape, reel tape, and cover tape) are not heat-resistant, and the packaging material may be deformed at temperatures of 120° ; $2 \times 90^{\circ}$ +8/-0°C, 24hours, 1times

The base tape can be baked together with the product at this temperature. Please pay attention to the uniformity of heat.

11 HANDLING CONDITIONS

• Be careful in handling or transporting products because excessive stress or mechanical shock may break products.

• Handle with care if products may have cracks or damages on their terminals. If there is any such damage, the characteristics of products may change. Do not touch products with bare hands that may result in poor solder ability and destroy by static electrical charge.

Cognizant of our commitment to quality, we operate our own factory equipped with state-of-the-art production facilities and a meticulous quality management system. We hold certifications for ISO9001, ISO14001, ISO27001, OHSA18001, BSCI.

Every product undergoes stringent testing, including transmit power, sensitivity, power consumption, stability, and aging tests. Our fully automated module production line is now in full operation, boasting a production capacity in the millions, capable of meeting high-volume production demands.

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14 RELATED DOCUMENTS

- MinewSemi_Product_Naming_Reference_Manual https://en.minewsemi.com/file/MinewSemi_Product_Naming_Reference_Manual_EN.pdf
- MinewSemi_Connectivity_Module_Catalogue https://en.minewsemi.com/file/MinewSemi_Connectivity_Module_Catalogue_EN.pdf



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